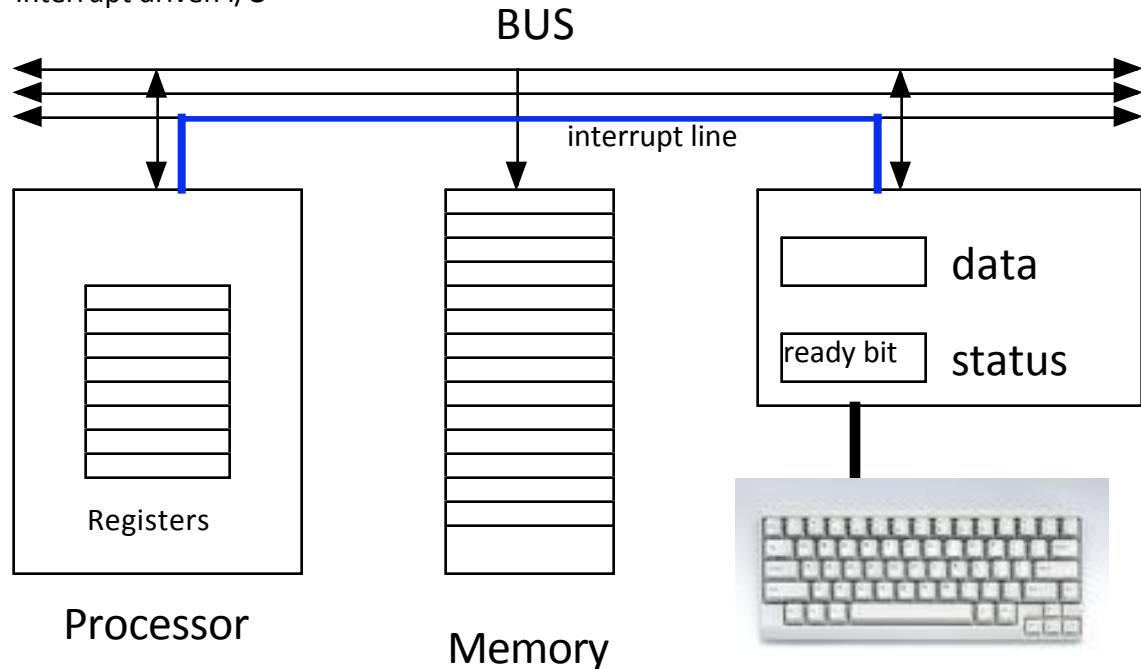


Interrupt driven I/O



CPU operation

acknowledge interrupt

L: ~~wait until ready is true (busy waiting),~~
input data from data register to CPU register;
~~reset ready to false;~~
go to L

Keyboard operation

{Upon pressing a key}
data register = key value;
~~set ready to true~~ **keyboard sends an interrupt signal to CPU**